| | Туре | L# | Hits | Search Text | DBs | Time Stamp | Comments |
|---|------|----|------|--|---|------------|----------|
| 1 | BRS | L1 | 138 | 'system management mode' and protected adj mode | US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TDB | | |
| 2 | BRS | L2 | 42 | 1 and mode adj interrupt | US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TDB | | |
| 3 | BRS | L3 | 0 | 2 and 'global descriptor table' | US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TDB | | |
| 4 | BRS | L4 | 0 | 2 and 'global descriptor' | US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TDB | | |
| 5 | BRS | L5 | 192 | 'global descriptor' | | | |
| 6 | BRS | L6 | 155 | 'global descriptor table' | US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TDB | | |

| _ | Туре | L# | Hits | Search Text | DBs | Time Stamp | Comments |
|----|------|-----|------|----------------------------------|-----|------------|----------|
| 7 | BRS | L7 | o | 2 and 5 | 1 ' | | |
| 8 | BRS | L8 | o | 2 and "52" and intel adj pentium | 1 ' | | |
| 9 | BRS | L9 | 0 | 2 and intel adj pentium | | | |
| 10 | BRS | L10 | 838 | intel adj pentium adj processor | 1 ' | | |
| 11 | BRS | L11 | 0 | 2 and 10 | | | |
| 12 | BRS | L12 | 0 | 2 and 'Intel Pentium' | | : | |

| | Туре | L# | Hits | Search Text | DBs | Time Stamp | Comments |
|----|------|-----|-------|---------------------------|---|---------------------|----------|
| 13 | BRS | L13 | 0 | 2 and 'Windows NT' | 1 . | | |
| 14 | BRS | L14 | 12049 | 'Windows NT' | US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TDB | | |
| 15 | BRS | L15 | 0 | 2 and 32-bit adj code | | 2005/04/07 14:30 | |
| 16 | BRS | L16 | 3 | 2 and 32-bit near3 code | US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TDB | | |
| 17 | BRS | L17 | 0 | 16 and state near3 saving | | | |
| 18 | BRS | L18 | 1 | 16 and kernel | | | |

| | Туре | L# | Hits | Search Text | DBs | Time Stamp | Comments |
|----|------|-----|------|-------------------------|-----|------------|----------|
| 19 | BRS | L19 | 1 | 16 and kernel and state | ' | | |